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JUN 26 2006IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Josef Bock et al.  
Serial No.: 10/521,106  
Filed: September 13, 2005  
Title: BIPOLAR TRANSISTOR

Examiner: Sara W. Crane  
Group Art Unit: 2811  
Docket No.: I435.121.101/12307US

CERTIFICATE OF TRANSMISSION

Commissioner for Patents  
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2. Supplemental Information Disclosure Statement (2 pgs.);
3. Form PTO-1449 (1 pg.); and
4. Copy of (2) cited references.

Date: June 26, 2006

PPK: cmj

By: 

Name: Paul P. Kempf (Reg. No. 39,727)

24 Pages (including cover page)

JUN 26 2006

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 Docket No.: 1435.121.101/12307US

Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Sir:

We are transmitting herewith the attached:

- Transmittal Sheet containing Certificate of Mailing under 37 C.F.R. 1.10
- Supplemental Information Disclosure Statement 2 pg(s).
- Form PTO-1449 1 pg(s).
- Copy of 2 cited references
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**CERTIFICATE OF FACSIMILE TRANSMISSION:**

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Name: Paul P. Kempf

JUN 26 2006

06/26/2006 16:31 FAX 6125732005

DICKE, BILLIG&CZAJA P.A.

003

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Josef Böck et al.

Serial No.: 10/521,106  
(Priority Application No. DE 102 31 407.1)  
(International Application No. PCT/EP2003/007553)

Filed: September 13, 2005  
(Priority Date July 11, 2002)  
(International Filing Date July 11, 2003)

Docket No.: I435.121.101/12307US

Title: BIPOLAR TRANSISTOR

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached form 1449 be considered by the Examiner and made of record. Any required copies of patents, publications or other documents are enclosed for the Examiner's review. Pursuant to the provisions of M.P.E.P. 609, Applicants further request a copy of the 1449 form, marked as being considered and initialed by the Examiner, be returned with the next Official Communication.

Since an Office Action has been received by Applicants, the Patent Office is hereby authorized to charge Deposit Account No. 500471 in the amount of \$180.00 to cover the submission of a Supplemental Information Disclosure Statement as set forth under 37 C.F.R. 1.17(p). However, at any time during the pendency of this application the Patent Office is hereby authorized to charge Deposit Account No. 500471 for any additional fees or credit any overpayments .

06/27/2006 TL0111 0000053 500471 10521106  
01 FC:1806 180.00 DA

**Supplemental Information Disclosure Statement**

Applicant: Josef Böck et al.

Serial No.: 10/521,106

(Priority Application No. DE 102 31 407.1)

(International Application No. PCT/EP2003/007553)

Filed: September 13, 2005

(Priority Date July 11, 2002)

(International Filing Date July 11, 2003)

Docket No.: I435.121.101/12307US

Title: BIPOLAR TRANSISTOR

Applicants respectfully request consideration of these references during prosecution of the above-identified matter. The Examiner is invited to contact the Applicants' representative at the below-listed telephone number if there are any questions regarding this Communication or the tendered references.

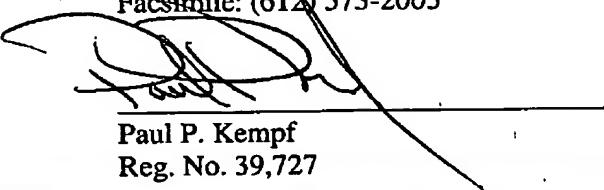
Respectfully submitted,

Josef Böck et al.,

By their attorneys,

DICKE, BILLIG & CZAJA, PLLC  
Fifth Street Towers, Suite 2250  
100 South Fifth Street  
Minneapolis, MN 55402  
Telephone: (612) 767-2502  
Facsimile: (612) 573-2005

Dated: Jun 26, 2006  
PPK:cmj

  
Paul P. Kempf  
Reg. No. 39,727**CERTIFICATE UNDER 37 C.F.R. 1.8:**

The undersigned hereby certifies that this paper or papers, as described herein, are being transmitted via facsimile to Facsimile No. (571) 273-8300 on this 26 day of June, 2006.

By:   
Name: Paul P. Kempf

JUN 26 2006

<b>FORM PTO-1449</b>		Docket No.: 1435.121.101/12307US	Serial No.: 10/521,106
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT(S) INFORMATION DISCLOSURE STATEMENT		Applicant: Josef Bock et al.	
		Filing Date: September 13, 2005	Group Art Unit: 2811

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date if Appropriate
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						
AL						

**FOREIGN PATENT DOCUMENTS**

	Document No.	Date	Country	Class	Sub Class	Translated Yes No (Abstract Only)
AM	0 535 350	8/1992	EPO			<input type="checkbox"/> <input checked="" type="checkbox"/>
AN						<input type="checkbox"/> <input type="checkbox"/>
AO						<input type="checkbox"/> <input type="checkbox"/>
AP						<input type="checkbox"/> <input type="checkbox"/>

**OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)**

AQ	Ning, Tak H., "Self-Aligned Bipolar Transistor for High-Performance and Low-Power-Delay VLSI," IEEE Transactions on Electron Devices, Vol. ED-28, No. 9, pp. 1010-1013 (September 1981).
AR	

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

Page 1 of 1